APR 0 1 2003

N THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Subhash C. Roy et al.

SERIAL NO.: 09/918,123

FILED: July 30, 2001

FOR: Real Time Debugger Interface

for Embedded Systems

Honorable Commissioner of Patents

and Trademarks

Washington, D.C. 20231

Sir:

GROUP ART UNIT:

2812

EXAMINER: Stacy

: Stacy Whitingre

ATT'Y DOCKET: TRA-040C1

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Wasington, D.C. 20231.

David P. Gordon Reg. No. 29,996 March 27, 2003

This is a timely reply to a Final Office Action dated January 8, 2003 (Paper #5). A Request for Continued Examination together with the appropriate fee is enclosed herewith.

AMENDMENT

Please amend the application as follows.

In the Claims

Please cancel claims 4-18 and add the following claims 19-3

- 19. A method of debugging a processor, said method comprising:
 - a) providing information about processor activity in real time;

and

b) associating the instructions executed by the processor with the information about processor activity, wherein